

Belegung des Pollin Evaluations- und ADD-ON-Boards

ADD-On Board		Eval. Board	J4 Pin	ATmega Pin	Mega16/32 Funktion	Mega8535 Funktion	ATmega8 Pin	ATtiny2313 Pin	ATtiny2313 Funktion	Eval. Board	ADD-On Board		Bemerkungen
DB0	PA1		1	40	PA0 (ADC0)	PA0 (ADC0)	23		PC0 (ADC0)		PA1	DB0	
DB1	PA2		2	39	PA1 (ADC1)	PA1 (ADC1)	24		PC1 (ADC1)		PA2	DB1	
DB2	PA3		3	38	PA2 (ADC2)	PA2 (ADC2)	25		PC2 (ADC2)		PA3	DB2	
DB3	PA4		4	37	PA3 (ADC3)	PA3 (ADC3)	26		PC3 (ADC3)		PA4	DB3	
DB4	PA5		5	36	PA4 (ADC4)	PA4 (ADC4)	27		PC4 (ADC4/SDA)		PA5	DB4	
DB5	PA6		6	35	PA5 (ADC5)	PA5 (ADC5)	28		PC5 (ADC5/SCL)		PA6	DB5	
DB6	PA7		7	34	PA6 (ADC6)	PA6 (ADC6)					PA7	DB6	
DB7	PA8		8	33	PA7 (ADC7)	PA7 (ADC7)					PA8	DB7	
E	PB1		9	1	PB0 (XCK/T0)	PB0 (XCK/T0)		12	PB0 (AIN0/PCINT0)		PB1	E	
RW	PB2		10	2	PB1 (T1)	PB1 (T1)		13	PB1 (AIN1/PCINT1)		PB2	RW	
RS	PB3		11	3	PB2 (INT2/AIN0)	PB2 (INT2/AIN0)	14		PB2 (OC0A/PCINT2)		PB3	RS	
	PB4		12	4	PB3 (OC0/AIN1)	PB3 (OC0/AIN1)	15		PB3 (OC1A/PCINT3)		PB4		
	PB5		13	5	PB4 (SS)	PB4 (SS)	16		PB4 (OC1B/PCINT4)		PB5		
	PB6		14	6	PB5 (MOSI)	PB5 (MOSI)	17		PB5 (MOSI/DI/SDA/PCINT5)		PB6		
	PB7		15	7	PB6 (MISO)	PB6 (MISO)	18		PB6 (MISO/DO/PCINT6)		PB7		
	PB8		16	8	PB7 (SCK)	PB7 (SCK)	19		PB7 (UCSK/SCL/PCINT7)		PB8		
	PC1		17	22	PC0 (SCL)	PC0 (SCL)					PC1		
	PC2		18	23	PC1 (SDA)	PC1 (SDA)					PC2		
	PC3		19	24	PC2 (TCK)	PC2					PC3		
n.c.	n.c.	n.c.	20							n.c.	n.c.	n.c.	
	PC4		21	25	PC3 (TMS)	PC3					PC4		
	PC5		22	26	PC4 (TDO)	PC4					PC5		
	PC6		23	27	PC5 (TDI)	PC5					PC6		
	PC7		24	28	PC6 (TOSC1)	PC6 (TOSC1)					PC7		
	PC8		25	29	PC7 (TOSC2)	PC7 (TOSC2)					PC8		
	PD1	RXD	26	14	PD0 (RXD)	PD0 (RXD)	2		PD0 (RXD)	RXD	PD1		
	PD2	TXD	27	15	PD1 (TXD)	PD1 (TXD)	3		PD1 (TXD)	TXD	PD2		
	PD3	Taster1	28	16	PD2 (INT0)	PD2 (INT0)	4		PD2 (INT0)	6	PD2 (CKOUT/XCK/INT0)	Taster1	PD3
	PD4	Taster2	29	17	PD3 (INT1)	PD3 (INT1)	5		PD3 (INT1)	7	PD3 (INT1)	Taster2	PD4
	PD5	Taster3	30	18	PD4 (OC1B)	PD4 (OC1B)	6		PD4 (XCK/TO)	8	PD4 (T0)	Taster3	PD5
	PD6	LED1	31	19	PD5 (OC1A)	PD5 (OC1A)	11		PD5 (T1)	9	PD5 (OC0B/T1)	LED1	PD6
	PD7	LED2	32	20	PD6 (ICP1)	PD6 (ICP1)	12		PD6 (AIN0)	11	PD6 (ICP)	LED2	PD7
	PD8	Buzzer	33	21	PD7 (OC2)	PD7 (OC2)	13		PD7 (AIN1)			Buzzer	PD8
	AREF		34	32	AREF	AREF	21		AREF			AREF	
	GND		35	11, 31	GND	GND	8, 22		GND	10	GND	GND	
	VCC		36	10, 30	VCC, AVCC	VCC, AVCC	7, 20		VCC, AVCC	20	VCC	VCC	
	GND		37	11, 31	GND	GND	8, 22		GND	10	GND	GND	
	VCC		38	10, 30	VCC, AVCC	VCC, AVCC	7, 20		VCC, AVCC	20	VCC	VCC	
	GND		39	11, 31	GND	GND	8, 22		GND	10	GND	GND	
	VCC		40	10, 30	VCC, AVCC	VCC, AVCC	7, 20		VCC, AVCC	20	VCC	VCC	

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ADD-On Board		Eval. Board	J4 Pin	ATtiny Pin	Tiny12 Funktion	Tiny15 Funktion	EEPROM 24Cxxx		Eval. Board	ADD-On Board	
							Pin				
DB4	PA5		5				5	SDA		PA5	DB4
DB5	PA6		6				6	SCL		PA6	DB5
	PB4		12	3	PB4 (XTAL2)	PB3 (ADC2)				PB4	
	PB5		13	2	PB3 (XTAL1)	PB4 (ADC3)				PB5	
	PB6		14	5	PB0 (MOSI/AIN0)	PB0 (AIN0/AREF/MOSI)				PB6	
	PB7		15	6	PB1 (MISO/INT0/AIN1)	PB1(AIN1/MISO/OC1A)				PB7	
	PB8		16	7	PB2 (SCK/T0)	PB2 (ADC1/SCK/T0/INT0)				PB8	
	PC1		17				6	SCL		PC1	
	PC2		18				5	SDA		PC2	
	GND		35	4	GND	GND	1, 2	A1, A2		GND	
	VCC		36	8	VCC	VCC	8	VCC		VCC	
	GND		37	4	GND	GND	3, 4	A3, GND		GND	
	VCC		38	8	VCC	VCC	8	VCC		VCC	
	GND		39	4	GND	GND	7	WP		GND	
	VCC		40	8	VCC	VCC	8	VCC		VCC	
	n.c.		n.c.	1	PB5 (Reset)	PB5 (Reset/ADC0)				n.c.	

I/O-Anschluß 25-pol. Sub-D					
ADD-On Pin	SubD-Pin		ADD-On Pin	SubD-Pin	
	1	13		14	25
	2	12		15	24
	3	11		16	23
	4	10		17	22
	5	9		18	21
	6	8		19	20
	7	7		20	19
	8	6		21	18
	9	5		22	17
	10	4		23	16
	11	3		24	15
	12	2		25	14
	13	1			